#5

6477032

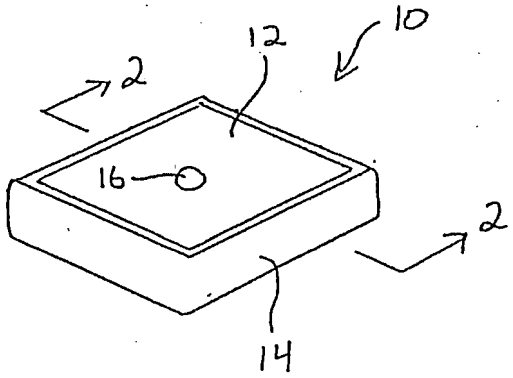

FIG. 1

8 Figs

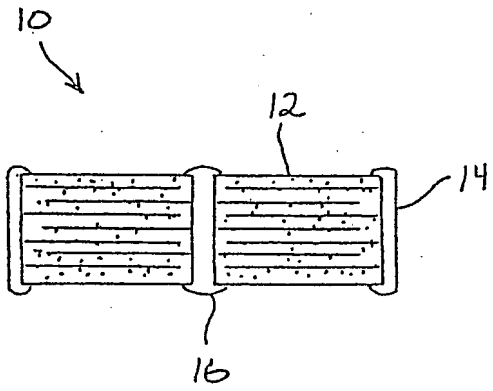

FIG. 2

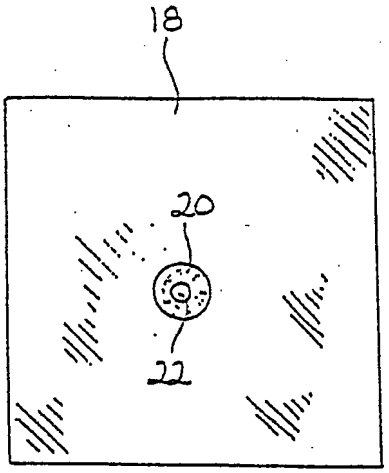

FIG. 3A

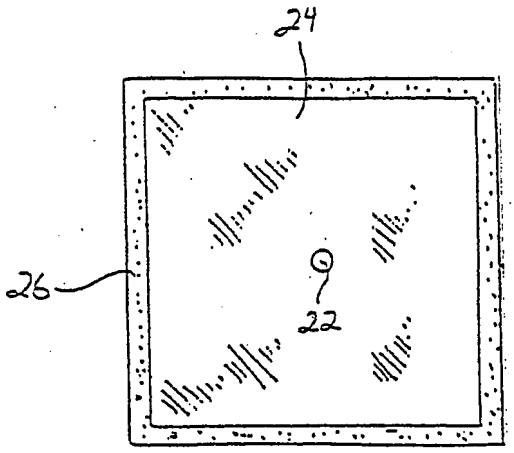

FIG. 3B

VIA LAYOUT FOR CONTACTS FOR MICRO PROCESSOR

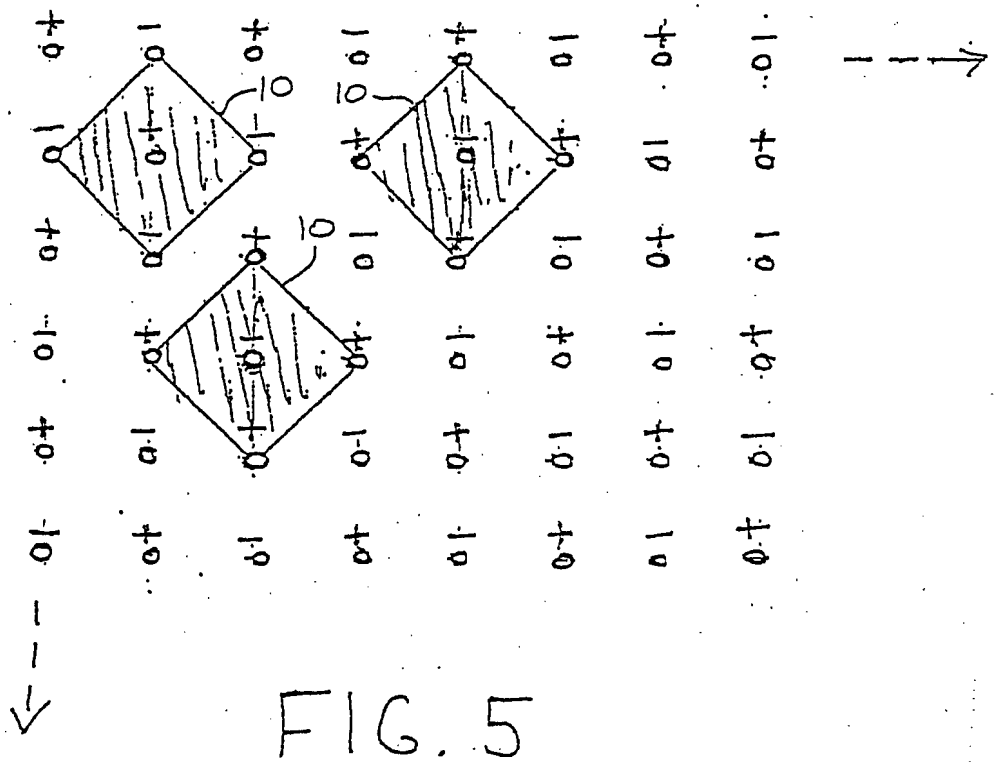

FIG. 5

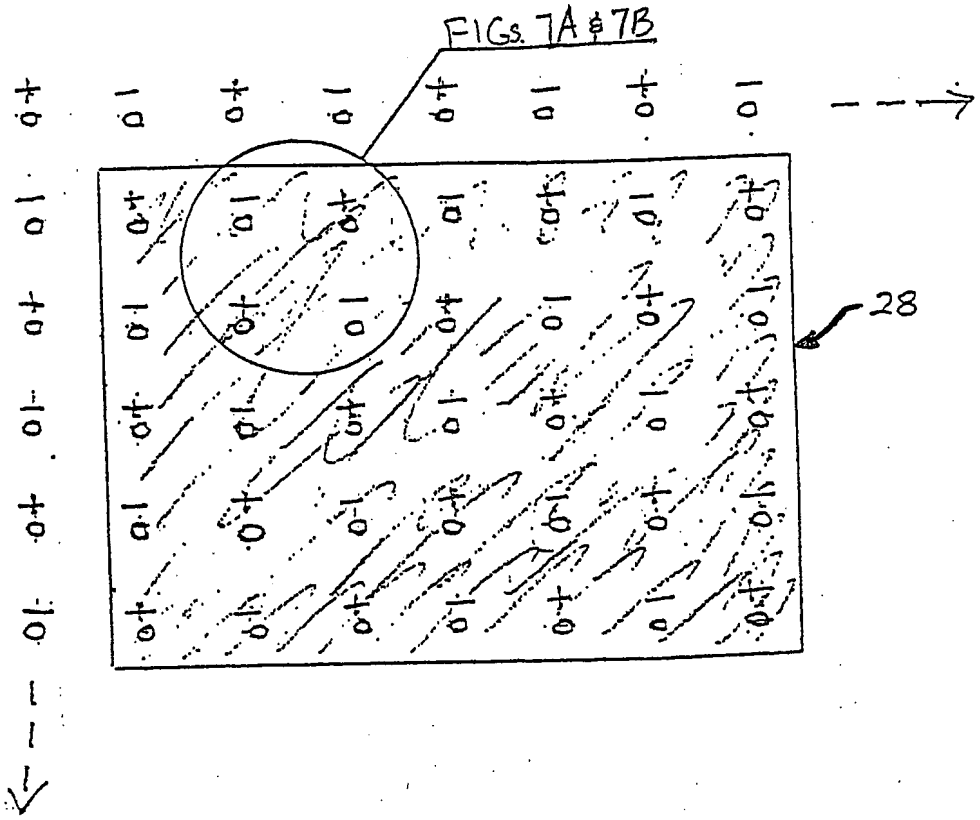

FIG. 6

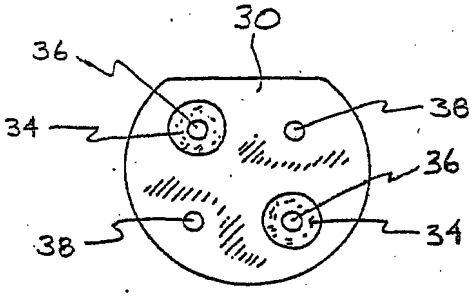

FIG. 7A

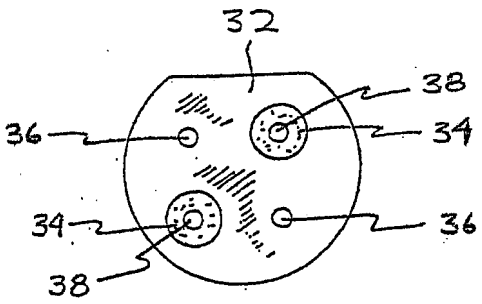

FIG. 7B